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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/508,915	04/21/2005	Hiroshi Miyagi	TIC-0073	8214

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EXAMINER
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WENDELL, ANDREW

ART UNIT	PAPER NUMBER
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2618

DATE MAILED: 04/06/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/508,915	MIYAGI, HIROSHI	
	<b>Examiner</b>	<b>Art Unit</b>	
	Andrew Wendell	2618	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on 27 April 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-4 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |                                                                                                    |                                                                             |
|----------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)                        | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)               | Paper No(s)/Mail Date. _____                                                |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____                                                                        | 6) <input type="checkbox"/> Other: _____                                    |

## **DETAILED ACTION**

### ***Drawings***

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the oscillator 17 (Fig. 1) connected to the control signal 23 (Fig. 1) must be shown or the feature(s) canceled from the claim(s) (claim 4 recites the limitation that is not shown in figure 1). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

***Claim Rejections - 35 USC § 103***

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1-2 and 4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Myer (US Pat# 5,012,490) in view of Lee et al. (US Pat# 5,774,555).

Regarding claim 1, Myer's varying bandwidth digital signal detector teaches a receiver which converts a received signal directly to a baseband signal (Col. 4 lines 55-62), comprising a switched-capacitor filter 405 (Fig. 4, Col. 5 lines 57-58) controlling a cutoff frequency when the baseband signal RBS (Fig. 4) is filtered according to a control signal FBC (Fig. 4) provided for a switched-capacitor element; an oscillator generating a periodic signal 425 (Fig. 4); and a periodic signal generated by the oscillator 425 (Fig. 4) according to the received signal, characterized in that an output signal from the control signal FBC (Fig. 4) for the switched-capacitor element 405 (Fig. 4, Col. 4 line 55-Col. 5 line 58). Myer fails to teach about a divider used for a control signal.

Lee et al. switched capacitor bandpass filter teaches a receiver, comprising a switched-capacitor filter 100 (Fig. 2) controlling a cutoff frequency when the signal is filtered according to a control signal fvco (Fig. 2) provided for a switched-capacitor element 100 (Fig.2); an oscillator 40 (Fig. 2) generating a periodic signal; and a divider 10 (Fig. 2) dividing a periodic signal generated by the oscillator 40 (Fig. 2)

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according to the received signal, characterized in that an output signal from the divider 10 (Fig. 2) is provided as the control signal  $f_{vco}$  (Fig. 2) for the switched-capacitor element 100 (Fig. 2).

Therefore, at the time the invention was made it would have been obvious to one skilled in the art to incorporate a divider used for a control signal as taught by Lee et al. into Myer's receiver in order to reduce costs and improve accuracy (Col. 1 lines 28-51).

Regarding claim 2, Lee et al. teaches the divider is a programmable counter and is a divider in a system of a division to an integral multiple or a fractional-N system (Col. 2 lines 1-48). It is well known that a divider is a programmable counter.

Regarding claim 4, Myer teaches a receiver which converts a received signal directly to a baseband signal (Col. 4 lines 55-62), comprising an oscillator 425 and 415 (Fig. 4, both the local oscillator and timing control get the same signal after the filter 405 and it gets feedback to the oscillators) generating a periodic signal; a mixer 403 (Fig. 4) for mixing a periodic signal generated by said oscillator 415 (Fig. 4, which is the same signal that gets feed into the controller 440) with the received signal, and outputting a baseband signal RBS (Fig. 4, Col. 4 lines 55-62); a switched-capacitor filter 405 (Fig. 4, Col. 5 lines 57-58) controlling a cutoff frequency when filtering the baseband signal output from said mixer 403 (Fig. 4) according to a control signal FBC (Fig. 4) provided for a switched-capacitor element 405 (Fig. 4); and a periodic signal generated by said oscillator 415 and 425 (Fig. 4, same feedback signal) according to the received signal, characterized in that the output signal from the control signal FBC

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(Fig. 4) for the switched-capacitor element 405 (Fig. 4, Col. 4 line 55-Col. 5 line 58).

Myer fails to teach about a divider used for a control signal.

Lee et al. switched capacitor bandpass filter teaches a receiver, comprising a switched-capacitor filter 100 (Fig. 2) controlling a cutoff frequency when the signal is filtered according to a control signal  $f_{vco}$  (Fig. 2) provided for a switched-capacitor element 100 (Fig. 2); an oscillator 40 (Fig. 2) generating a periodic signal; and a divider 10 (Fig. 2) dividing a periodic signal generated by the oscillator 40 (Fig. 2) according to the received signal, characterized in that an output signal from the divider 10 (Fig. 2) is provided as the control signal  $f_{vco}$  (Fig. 2) for the switched-capacitor element 100 (Fig. 2).

Therefore, at the time the invention was made it would have been obvious to one skilled in the art to incorporate a divider used for a control signal as taught by Lee et al. into Myer's receiver in order to reduce costs and improve accuracy (Col. 1 lines 28-51).

4. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Myer (US Pat# 5,012,490) in view of Lee et al. (US Pat# 5,774,555) as applied to claim 1 above, and further in view of Kultgen et al. (US Pat# 4,760,346).

Regarding claim 3, Myer's varying bandwidth digital signal detector in view of Lee et al. switched capacitor bandpass filter teaches the limitations in claim 1. Both Lee et al. and Myer fail to teach a resistor element inside the switched-capacitor filter.

Kultgen et al. switched capacitor summing amplifier teaches switched-capacitor comprises at least an amplifier, and a resistor element  $R_{sub f}$  (Fig. 3b), which

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functions as a feedback resistor of the amplifier, is realized by the switched-capacitor element (Fig. 3b).

Therefore, it would have been obvious at the time of the invention to one of ordinary skill in the art at the time the invention was made to incorporate a resistor element inside the switched-capacitor filter as taught by Kultgen et al. into a divider used for a control signal as taught by Myer's in view of Lee et al. receiver in order to reduce costs and improve accuracy (Col. 1 lines 28-51).

### ***Conclusion***

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Souissi et al. discloses an apparatus for frequency agile variable bandwidth transceiver. Heath et al. discloses an apparatus for providing an analog waveform synchronized with an input signal.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew Wendell whose telephone number is 571-272-0557. The examiner can normally be reached on 7:30-5 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nay Maung can be reached on 571-272-7882. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Patent Examiner

Date: 3/27/2006

  
NAY MAUNG  
SUPERVISORY PATENT EXAMINER

ASW